

Application No. 10/646988 (Docket: CNTR.2209)  
37 CFR 1.111 Amendment dated 05/09/2006  
Reply to Office Action of 02/09/2006

**AMENDMENTS TO THE CLAIMS**

Kindly amend claims 1, 9, and 14 as shown in the following listing of claims. The listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims**

1. (Currently Amended) An apparatus, for managing power consumption of a microprocessor comprising:  
  
a plurality of functional units each including a corresponding plurality of activity outputs, for indicating when a respective functional unit is enabled;  
  
utilization assessment logic, coupled to said plurality of activity outputs, for assessing activity thereof to determine a current total power consumption value for the microprocessor;  
  
power control logic, coupled to said utilization assessment logic, for comparing said current total power consumption value with a threshold power value included in a specified power profile, wherein said specified power profile is selected from a plurality of power profiles stored within said power control logic; and  
  
a power consumption controller, coupled to said power management logic and said plurality of functional units, for engaging one of a plurality of power reduction modes if said current total power consumption value exceeds said threshold power value.
2. (Original) The apparatus as recited in claim 1, wherein one of said plurality of functional units is a cache and one of said plurality of power reduction modes is disabling said cache.
3. (Original) The apparatus as recited in claim 1, wherein one of said plurality of functional units is a branch prediction unit and one of said plurality of power reduction modes is disabling said branch prediction unit.